



### • General Description

The ZMD68301N combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

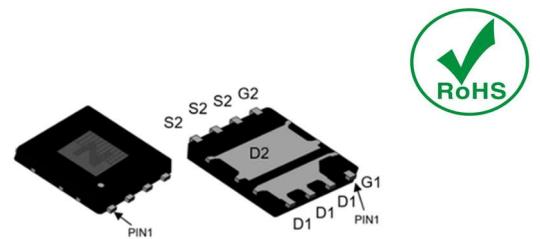
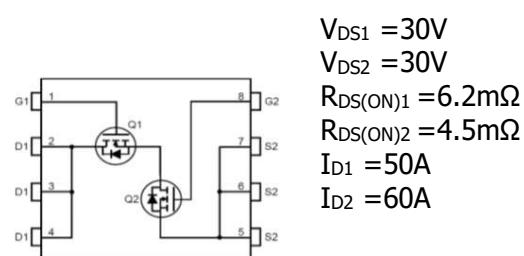
### • Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Dual DIE in one package

### • Application

- DC/DC Converters in Computing
- Isolated DC/DC Converters in Telecom and Industrial

### • Product Summary



DFN5 x 6

### • Ordering Information:

Part NO.	ZMD68301N		
Marking	ZMD68301		
Packing Information	REEL TAPE		
Basic ordering unit (pcs)	3000		

### • Absolute Maximum Ratings ( $T_c = 25^\circ C$ ) (Q1)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D@T_c=25^\circ C$	50	A
	$I_D@T_c=75^\circ C$	38	A
	$I_D@T_c=100^\circ C$	31.5	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	120	A
Total Power Dissipation( $T_c=25^\circ C$ )	$P_D@T_c=25^\circ C$	60	W
Total Power Dissipation( $T_A=25^\circ C$ )	$P_D@T_A=25^\circ C$	1.8	W
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ C$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ C$
Single Pulse Avalanche Energy@ $L=0.1mH$	$E_{AS}$	45	mJ
Avalanche Current@ $L=0.1mH$	$I_{AS}$	30	A


**•Thermal resistance(Q1)**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	2.1	° C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	70	° C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	° C

**•Electronic Characteristics(Q1)**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30			V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2		2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1.0	uA
Gate- Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V ,V <sub>DS</sub> =0V			±100	nA
Static Drain-source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =15A		6.2	8.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		8.9	13	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =25V, I <sub>D</sub> =10A		12		s
Source-drain voltage	V <sub>SD</sub>	I <sub>S</sub> =15A			1.28	V

**•Electronic Characteristics(Q1)**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V f = 1MHz	-	1150	-	pF
Output capacitance	C <sub>oss</sub>		-	235	-	
Reverse transfer capacitance	C <sub>rss</sub>		-	120	-	

**•Gate Charge characteristics(T<sub>a</sub> = 25°C)(Q1)**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q <sub>g</sub>	V <sub>DD</sub> = 15V I <sub>D</sub> = 15A V <sub>GS</sub> = 10V	-	18	-	nC
Gate - Source charge	Q <sub>gs</sub>		-	7	-	
Gate - Drain charge	Q <sub>gd</sub>		-	6	-	

•Absolute Maximum Ratings ( $T_c = 25^\circ\text{C}$ ) (Q2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D@TC=25^\circ\text{C}$	60	A
	$I_D@TC=75^\circ\text{C}$	45.6	A
	$I_D@TC=100^\circ\text{C}$	37.8	A
Pulsed Drain Current <sup>①</sup>	$I_{DM}$	135	A
Total Power Dissipation( $TC=25^\circ\text{C}$ )	$P_D@TC=25^\circ\text{C}$	50	W
Total Power Dissipation( $TA=25^\circ\text{C}$ )	$P_D@TA=25^\circ\text{C}$	1.8	W
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ\text{C}$
Single Pulse Avalanche Energy@ $L=0.1\text{mH}$	$E_{AS}$	125	mJ
Avalanche Current@ $L=0.1\text{mH}$	$I_{AS}$	50	A

## •Thermal resistance(Q2)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}$	-	-	2.5	$^\circ\text{C/W}$
Thermal resistance, junction - ambient	$R_{thJA}$	-	-	70	$^\circ\text{C/W}$
Soldering temperature, wavesoldering for 10s	$T_{sold}$	-	-	265	$^\circ\text{C}$

## •Electronic Characteristics(Q2)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	1.2		2.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=30\text{V}, V_{GS} = 0\text{V}$			1.0	$\mu\text{A}$
Gate- Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20\text{V}, V_{DS} = 0\text{V}$			$\pm 100$	nA
Static Drain-source On Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=20\text{A}$		4.5	6	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=10\text{A}$		6.5	9	$\text{m}\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS} = 25\text{V}, I_D=10\text{A}$		18		s
Source-drain voltage	$V_{SD}$	$I_S=20\text{A}$			1.28	V

**•Electronic Characteristics(Q2)**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	$C_{iss}$	V <sub>DS</sub> =25V f = 1MHz	-	1650	-	pF
Output capacitance	$C_{oss}$		-	330	-	
Reverse transfer capacitance	$C_{rss}$		-	220	-	

**•Gate Charge characteristics( $T_a = 25^\circ\text{C}$ )(Q2)**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	$Q_g$	$V_{DD} = 15\text{V}$ $I_D = 20\text{A}$ $V_{GS} = 10\text{V}$	-	29	-	nC
Gate - Source charge	$Q_{gs}$		-	12	-	
Gate - Drain charge	$Q_{gd}$		-	11	-	

Note: ① Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$  ;



- Channel characteristics curve(Q1)

Fig.1 Gate-Charge Characteristics

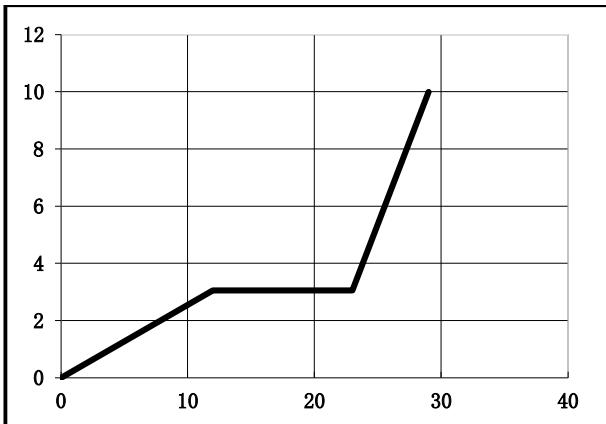


Fig.2 Capacitance Characteristics

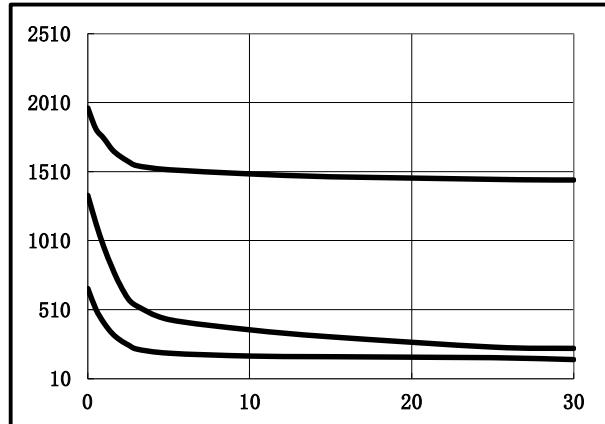


Fig.3 Power Dissipation

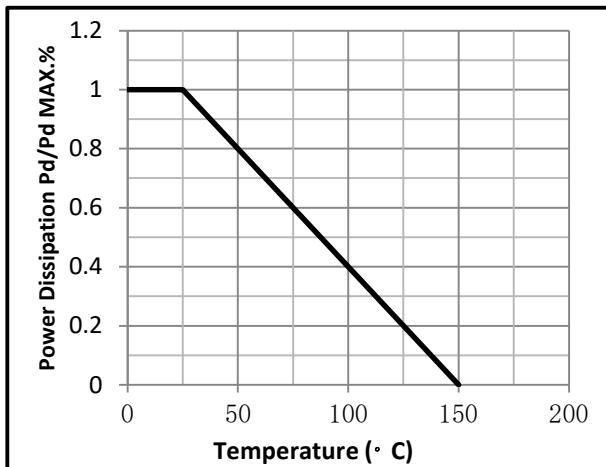


Fig.4 Typical output Characteristics

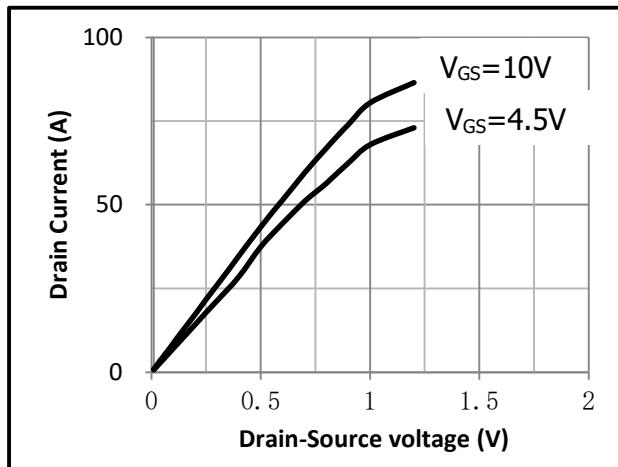


Fig.5 Threshold Voltage V.S Junction Temperature

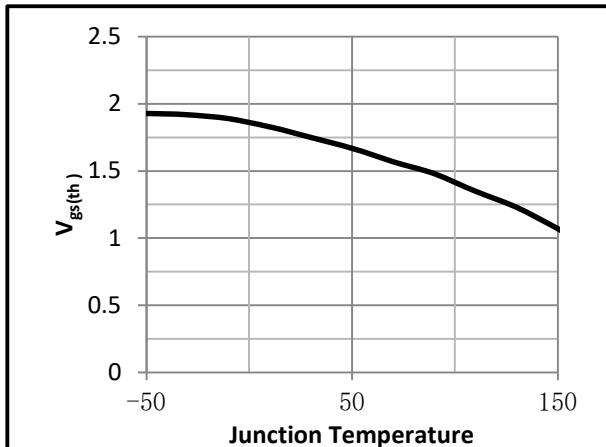


Fig.6 Resistance V.S Drain Current

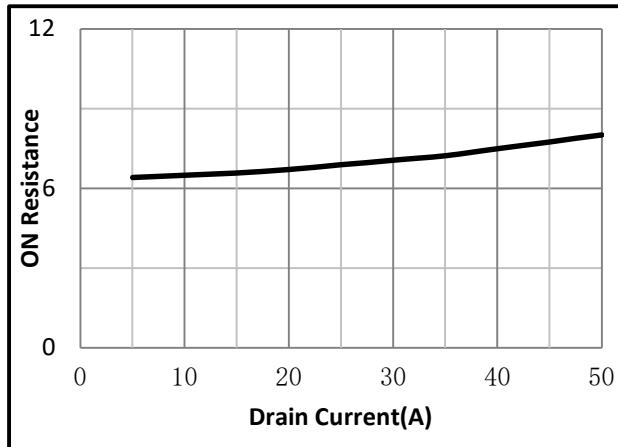




Fig.7 On-Resistance VS Gate Source Voltage

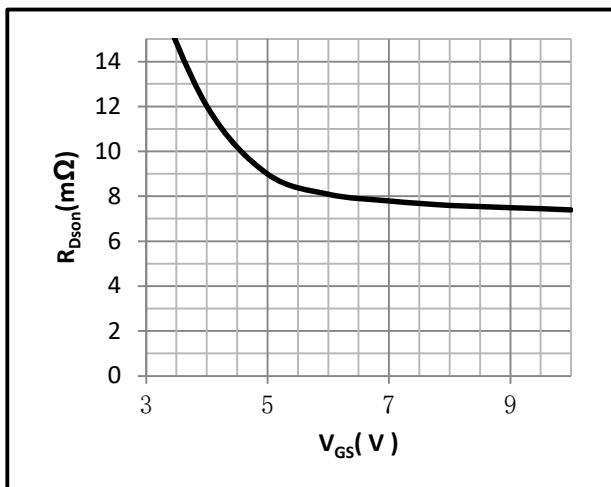


Fig.8 On-Resistance V.S Junction Temperature

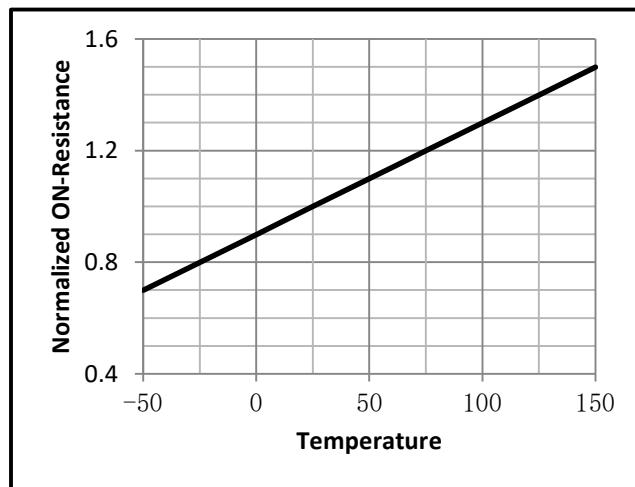
**• Channel characteristics curve(Q2)**

Fig.9 Gate-Charge Characteristics

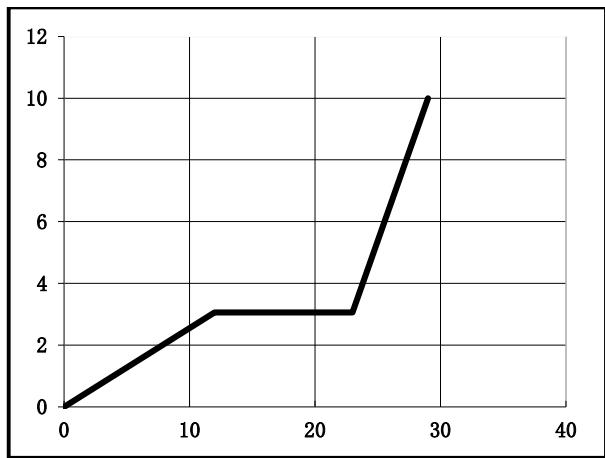


Fig.10 Capacitance Characteristics

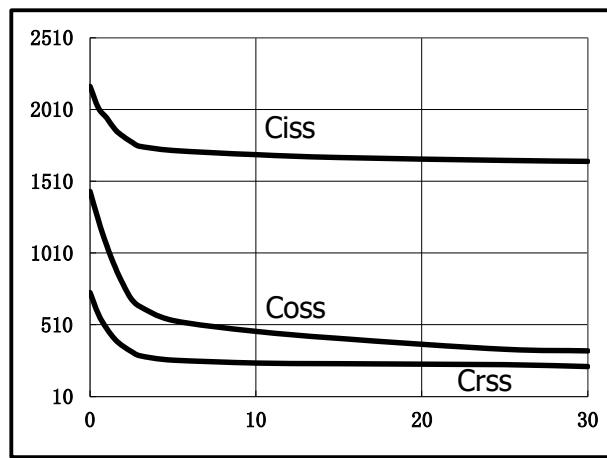


Fig.11 Power Dissipation

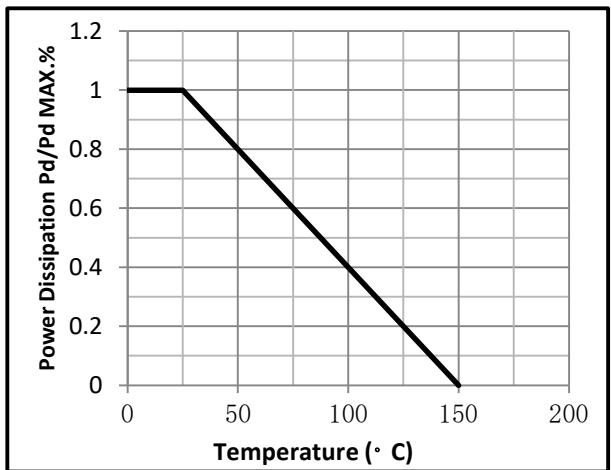


Fig.12 Typical output Characteristics

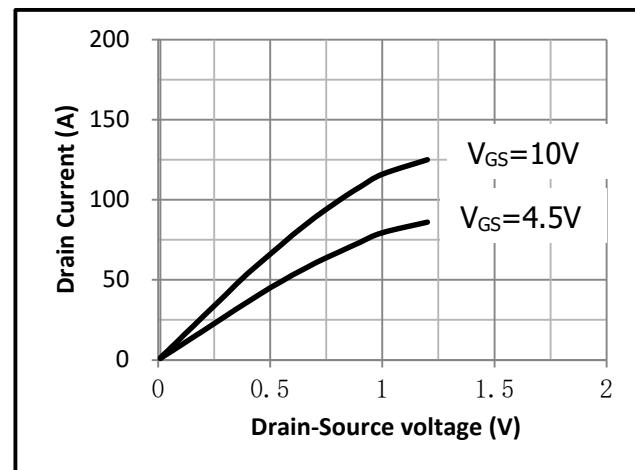




Fig.13 Threshold Voltage V.S Junction Temperature

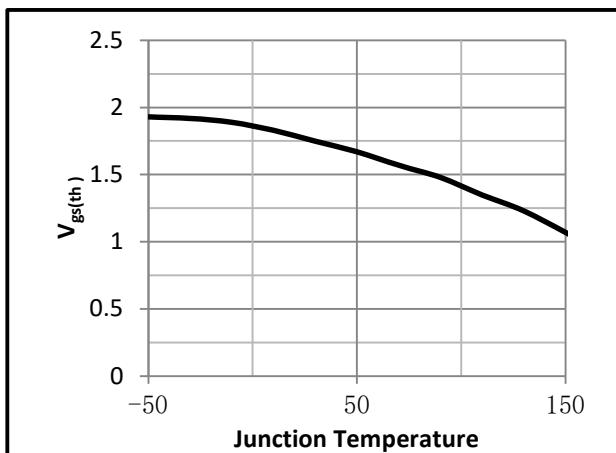


Fig.14 Resistance V.S Drain Current

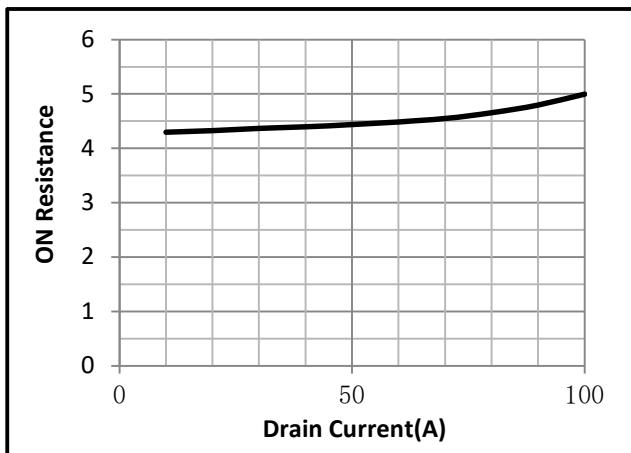


Fig.15 On-Resistance VS Gate Source Voltage

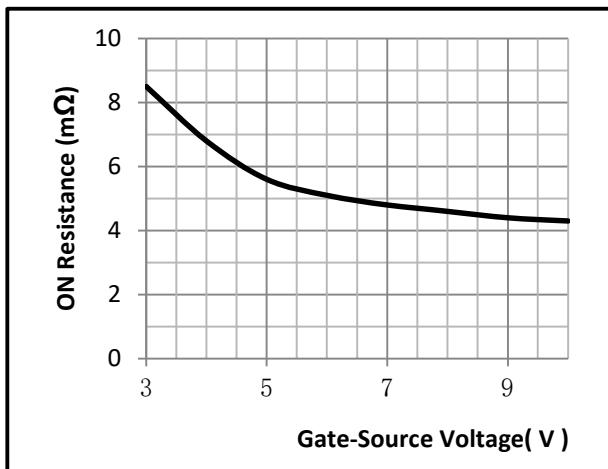


Fig.16 On-Resistance V.S Junction Temperature

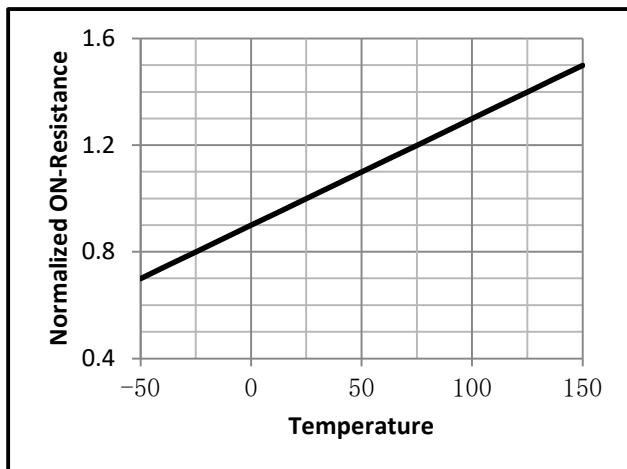


Fig.17 Gate Charge Measurement Circuit

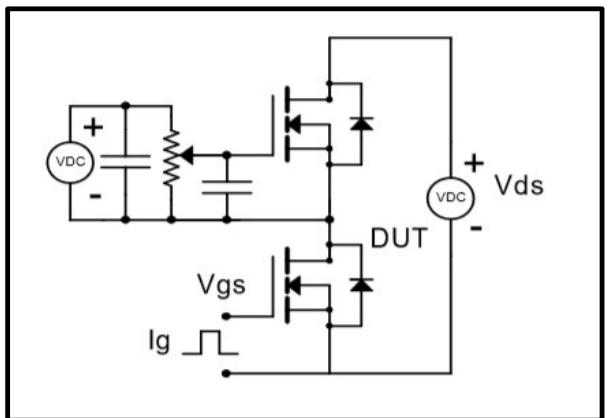


Fig.18 Gate Charge Waveform

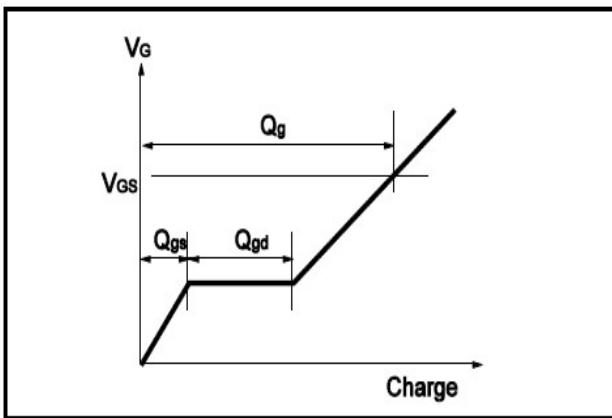




Fig.19 Switching Time Measurement Circuit

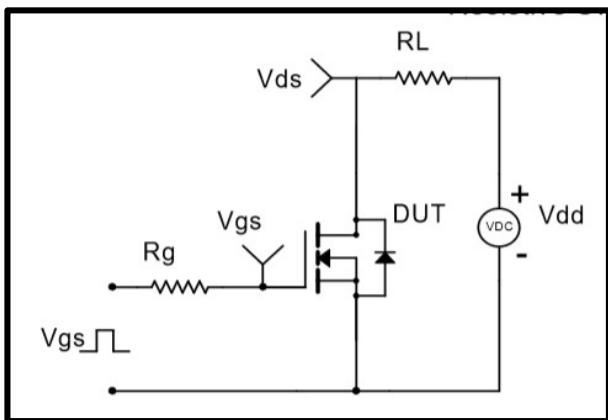


Fig.20 Switching Time Waveform

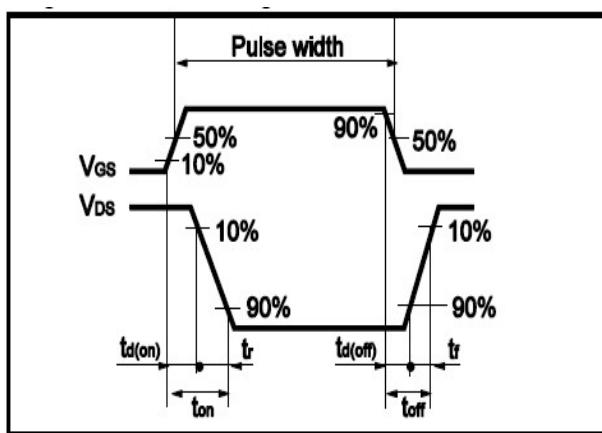


Fig.21 Avalanche Measurement Circuit

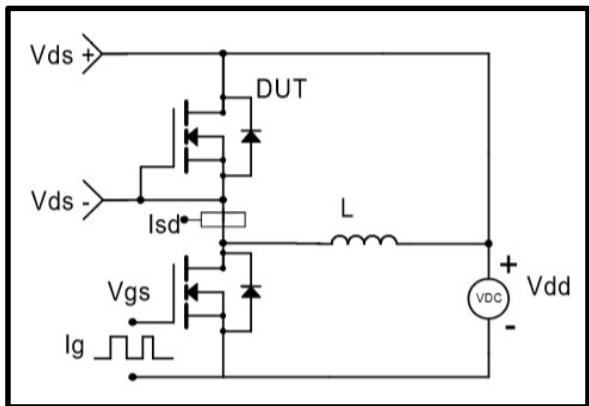
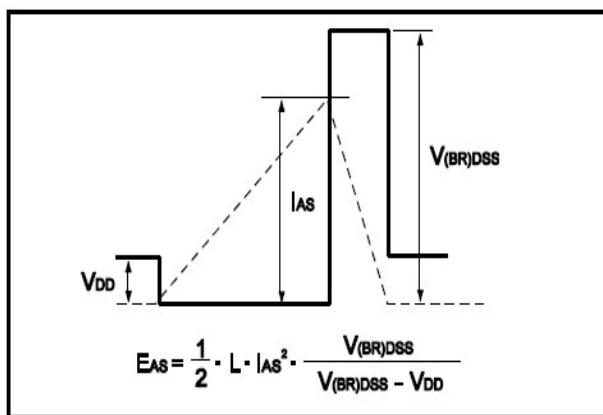


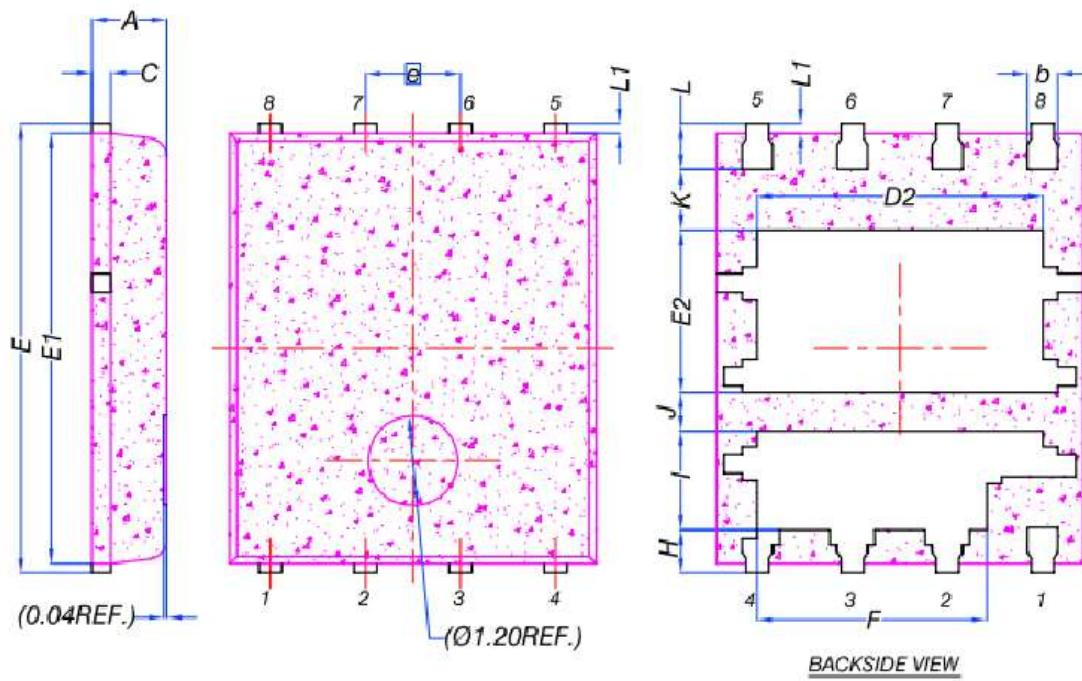
Fig.22 Avalanche Waveform



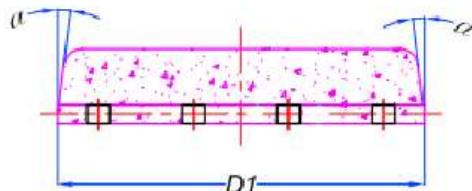


## •Dimensions (DFN5x6)

Unit: mm



BACKSIDE VIEW



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	2.02	2.17	2.32
e	1.27 BSC		
F	2.87	3.07	3.22
H	0.48	0.58	0.68
I	1.22	1.32	1.42
J	0.40	0.50	0.60
K	0.50	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
α	0°	-	12°